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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Averett, Guy E. et al.  
Serial No.: 10/072,145  
Filing Date: 02/07/2002  
Group Art Unit: 2811  
Examiner: Magee, Thomas J.  
Title: SEMICONDUCTOR DEVICE AND METHOD OF PROVIDING REGIONS OF LOW SUBSTRATE CAPACITANCE

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING FACSIMILE  
TRANSMITTED TO THE PATENT AND TRADEMARK OFFICE, ATTN: EXAMINER Thomas  
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